Link do produktu: https://www.gotronik.pl/emulator-mikrokontrolerow-avr-atmel-zgodny-z-jtagice-mk-ii-p-2857.html



## Emulator mikrokontrolerów AVR Atmel zgodny z JTAGICE mk II

Cena brutto	999,00 zł
Cena netto	812,20 zł
Dostępność	Na zamówienie
Czas wysyłki	14 dni
Numer katalogowy	JTAGICE-MKII

## Opis produktu

opis produkta
Tis item is 100% compatible with Atmel's AT JTAGICE mkll, It supports AVR32 series of MCUs and can upgrade with avrstudio's upgrade tool.
a powerful development and for the child philologype of all ARM S bit MEST MICKs and ARMS 32 bit CORPINCOs with MEST 1.141 Completed FIFIC instruction.  Combined with ARM Studio the ITARICE and in On-Chip Chilologype on all ARM S-bit EST Compositional visib. 1786 Disturbes or debugli
THG  The TRIC Interface is a 4-law Test Across Part (TMF) controller that is complicat with the IEEE 116-31 standard. The IEEE dandard was developed to enable a dandard way for efficiently test cross based connectivity (Soundary Soan). All and devices have extended this functionally in include Mill Programming and On-Dip Sobsoging support.
The ITAGICS midd uses the standard ITAG interface to enable the user to do real time emulation of the microcontroller while it is receiving in the largest system.
The ANK Ox-Obj Debug (ANXXXX) protected gives the ware completes central of the internal resources of the ANK intersectability. The XXXXXX retail gives accounts emulation at a factor of the cast of inditional emulators.
Administration in the Conference adds a new way of driving Ch. Chip Chibug. The debugliCEE Chi-chip debug replace uses a one-wire, to-directional interface to control the program flow, execute AFK indirections in the CFU and to program the different non-voidals resembles.
When the debugstills Enable (DRIRD) have by programmed and Lack bits are us-programmed, the debugstills eyelern within the larged deriva is activated. The ESSET port pin is configured as a trin-AID (spen-durst) is-directioned (40 pin with jud-up wealthed and becomes the communication galaxies pleases barged and emulator.
little that debuggitted is a debugging interface only and not a programming interface.
159 The FMSICE mild also supports foll programming through the 15P interface. At 71Ms and debuglitifit parts are supported. See Programming with TMSICE mild for more information.
The marks are selline tradition and market and appear.  TAGGIC middl features
NOT Student Commentation (ANS Student 4.09 or heart)  Support and ANS Concress and Industry Mills Student 4.09 or heart 1.00 or
S-NA MINE: The 17-00-CET stall date support decisions with debuggited 2 finderface, debuggited 2 models on excipt debugg of AVX interconvenienters in varied per count packages, using only a single in the debuggited period. The 47-00-CET stall control on the counter of intercontrol on any counter of intercontr
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